

Application No.: 10/073,261Docket No.: 1509-275**AMENDMENTS TO THE SPECIFICATION:**

*Please replace paragraph beginning on page 4, line 21, with the following amended paragraph:*

There may be a controller associated with the trusted clock. The controller may have controller logic running thereupon. There may be means of checking the veracity of the controller logic. The controller logic may be time-stamped. The controller logic may be time-stamped prior to passing data through the trusted clock. The controller logic signature may be checked prior to the time-stamping of data. This prevents the downloading of fake control data into the controller (known as spoofing) thereby preventing alteration of the clock time.

*Please replace paragraph beginning on page 10, line 15, with the following amended paragraph:*

In one embodiment of the present invention, shown in Figure 3, data from data source 10 is passed into a storage device 12. The storage device 12 (with its boundary shown as 13) comprises an interface 14, a data buffer 16, a secure controller 18 with an associated trusted clock/signature module 20, and data storage media 22a, 22b, 22c.